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K.K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation, Wiley, NY 1999. Amazon Link. Also translated to Chinese Slides Chapter 2 Chapter 3 Chapter 4 Chapter 5 Chapter 6 Chapter 7 Chapter 8 Chapter 9 Chapter 10 Chapter 11 Chapter 13 Chapter 14 Chapter 15 Chapter 17

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Dr. Parhi is widely recognized for his work on high-level transformations of iterative data-flow computations, for developing a formal theory of computing for design of digital signal processing systems, and for contributions to multi-gigabit transceivers for wired systems such as ethernet on copper and fiber and for backplanes, and for ...

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DSP systems can be realized using programmable processors or custom designed hardware circuits fabricated using very-large-scale-integrated (VLSI) circuit technology. The goal of digital design is to maximize the performance while keeping the cost down.

1.1 Introduction - VLSI Digital Signal Processing Systems ...

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VLSI digital signal processing systems: design and implementation. KK Parhi. John Wiley & Sons, 1999. 2347 * ... KJ Cho, KC Lee, JG Chung, KK Parhi. IEEE Transactions on Very Large Scale Integration (VLSI) Systems 12 (5), 522-531, 2004. 233: 2004:

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